




**JSS Mahavidyapeetha**  
**JSS Academy of Technical Education, Noida**  
**Department of Electronics & Communication Engineering.**



**FACULTY PROFILE**

**1. Personal Details**

NAME	Mr. ANURANJAN KANSAL	
DEPARTMENT	ELECTRONICS & COMMUNICATION ENGINEERING	
DESIGNATION	ASSISTANT PROFESSOR	
PHONE	9582144010	
EMAIL ID	anuranjankansal@jssaten.ac.in	
Date of Joining (JSSATEN)	12-03-2010	

**2. Experience**

Total Experience in Years: 22	Teaching: 22	Industry: Nil	Research: Nil
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**3. Qualifications**

COURSES	SPECIALIZATION	Year of Award	INSTITUTION	UNIVERSITY
B.E.	Electronics and Communication Engineering	2000	FET, RBS College, Agra	Dr. B R Ambedkar University, Agra
M.Tech.	Digital Communication	2007	JSSATE, Noida	UPTU, Lucknow
Ph.D.	RF Trap	Pursuing	Manav Rachna Univercity, Faridabad.	Manav Rachna Univercity, Faridabad.
Post Doc.	-	-	-	-

**4. Research & Publications**

Papers Published in Web of Science indexed Journals	International: 01	National: 00
Papers Published in SCOPUS indexed Journals	International: 00	National: 00

Papers Published in other Journals	International: 00	National: 00
Papers Presented in Conferences / Symposium	International: 02	National: 00
Books / Book chapters Published	Name of the book: NIL Publisher: Year of Publication:	

## 5. Research Guidance

PhD Guide? Give field & University	No	NA
Ph.D.s / Projects Guided	Ph.D. Awarded: No Guiding: NIL	Projects at Master's Level: 02 Projects at Bachelor's Level: 44

## 6. Grants

### i. Funds Received (Projects)

Project Name	Grant Amount	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
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### ii. Patents

Sl. No.	Title Details	Details of award of patent / Filed
-	-	-

### iii. Consultancy

Title of the work	Amount in Rs.	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
-	-	-	-	-

## 7. Awards Received

Awards "Best Paper Award"	Nil
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## 8. Publications

### i. International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	Field Imperfection Induced Effects in a Planar Dual Frequency Paul Trap	Anuranjan Kansal, Varun Saxena, Charu Pathak	IEEE TRANSACTION S ON PLASMA SCIENCE	Volume-50 Issue-4	Both	1.222	1

### ii. National Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
-	-	-	-	-	-	-	-

### iii. Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Conference	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1.	Unequal Amplitudes of Harmonic Terms in Particle Dynamics inside Paul Trap	Anuranjan Kansal, Charu Pathak, Varun Saxena	IEEE Xplore digital Library	978-1-7281-9744-9/20/\$31.00 ©2020 IEEE	YES	-	INOCON-2020
2.	Dynamics of Charged Particles in Dual Frequency Paul trap due to Additional External Forces	Anuranjan Kansal, Charu Pathak, Varun Saxena	IEEE Xplore digital Library	978-1-7281-8876-8/21/\$31.00 ©2021 IEEE	YES	-	I2CT-2021

### iv. Workshops /Conferences Attended

Sl. No.	Name of the workshop / Conference	Organizer	Date
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1	Microcontroller Design Workshop MSP430 platform	ABESIT Ghaziabad	4 <sup>th</sup> – 6 <sup>th</sup> December-2017
2	Computer Programming	IIT Bombay (Online) at Mahatma Gandhi Missions College Of Engineering & Technology, Sector-62, Noida	16 <sup>th</sup> – 21 <sup>th</sup> June-2014
3	Signals & Systems	IIT Kharagpur (online) at Mahatma Gandhi Missions College of Engineering & Technology, Sector-62, Noida	2 <sup>nd</sup> – 12 <sup>th</sup> January-2014
4	Analog Electronics	IIT Kharagpur (online) at Mahatma Gandhi Missions College of Engineering & Technology, Sector-62, Noida	4 <sup>th</sup> – 14 <sup>th</sup> June-2013
5	Basic Idea Lab FDP	AJAY KUMAR GARG ENGINEERING COLLEGE, Ghaziabad	2 <sup>nd</sup> - 6 <sup>th</sup> May-2022

**v. Workshops / Conference (Organized)**

Sl. No.	Name of the workshop / Conference	Organized by	Date	Role
1.	Next Generation Wireless Systems & Standards	JSSATE, Noida	29 <sup>th</sup> February – 5 <sup>th</sup> March-2016	Organizer

**vi. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)**

Sl. No.	Name of the workshop / Conference	Organizer	Date
1.	Next Generation Nano Satellite Communication Systems	JSSATE, Noida	7 <sup>th</sup> May to 11 <sup>th</sup> May, 2018
2.	Analog Integrated Circuit Design Using Cadence Analog Design Flow	JSSATE, Noida	27 <sup>th</sup> August to 01 <sup>st</sup> September, 2018

### 9. Details of NPTEL / COURSERA courses completed

Sl. No.	Name of the subject	Organized by	Date of completion / Award	Grade / Marks
1.	Matlab Programming for Numerical Computation	NPTEL	Jan-March-2020	87%
2.	Machine Learning	NPTEL	Feb-April-2021	77%
3.	An Introduction to Artificial Intelligence	NPTEL	Jan-April-2022	55%

### 10. Membership of Professional Bodies:

1. The Institution of Engineers (India) Member M-144255-9
2. Institute of communication Engineers and Information Technologists ICEIT Senior member (SMICEIT) SM-1100214 L

### 11. Any other information you will like to share about your professional experience

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